IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| In re A | Application of: |) | T- 1 | at 1 at 11 tr | |
|------------------------------------|-----------------------------------------------------------------------------------------------------------------------------|------------|------------|------------------|--|
| | Kenneth S. McElvain |) | | Clark, Sheila V. | |
| Application No.: 10/626,031 | |) | Art Unit: | 2823 | |
| Filing Date: July 23, 2003 | |) | Confirmati | on No. 1272 | |
| For: | INTEGRATED CIRCUIT DEVICES AN METHODS AND APPARATUSES FOR DESIGNING INTEGRATED CIRCUIT DEVICES | | | | |
| P.O. E | nissioner for Patents Box 1450 ndria, Virginia 22313-1450 | / | | | |
| SUBMISSION OF REPLACEMENT DRAWINGS | | | | | |
| Dear S | Sir: | | | | |
| eight (| Transmitted herewith is a partial se (8) sheets and eleven (11) figures for | | ` - | , | |
| | Respectfully submitted, | | | | |
| | BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN | | | | |
| Date: | Date: Dec. 2, 2010 James C. Scheller, Jr. Reg. No. 31,195 | | | | |
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| | CERTIFICATE OF | | | | |
| electron | certify that this correspondence is being deposite ic filing through the United States Patent and Tra- tember 2, 2010 | | | | |
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| | Signature | | | Date | |